



TEST SPECIFICATION

Complementary Silicon Plastic Power Transistor

TIP36A/TIP36B/TIP36C

Package: TO-3P

ISSUE DATE:2000/4/26 REV:A

Parameter	Test Condition	Test Specification	
		Min	Max
V _{CEO(SUS)}	I _C =30mA _{dc} , I _B =0, TIP36A	60V	-
	I _C =30mA _{dc} , I _B =0, TIP36B	80V	-
	I _C =30mA _{dc} , I _B =0, TIP36C	100V	-
I _{CEO}	V _{CE} =30V _{dc} , I _B =0, TIP36A	-	1mA
	V _{CE} =60V _{dc} , I _B =0, TIP36B/TIP36C	-	1mA
I _{CES}	V _{CE} =60V, V _{EB} =0, TIP36A	-	0.7mA
	V _{CE} =80V, V _{EB} =0, TIP36B	-	0.7mA
	V _{CE} =100V, V _{EB} =0, TIP36C	-	0.7mA
I _{EBO}	V _{BE} =5V, I _C =0, TIP36A/TIP36B/TIP36C	-	1.0mA
h _{FE}	I _C =1.5A, V _{CE} =4V	25	-
	I _C =15A, V _{CE} =4V	15	75
V _{CE(sat)}	I _C =15A, I _B =1.5A	-	1.8V
	I _C =25A, I _B =5A	-	4V
V _{BE(on)}	I _C =15A, V _{CE} =4V	-	2.0V
	I _C =25A, V _{CE} =4V	-	4.0V

MARKING:

