



TEST SPECIFICATION

Plastic Medium-Power Complementary Silicon Transistor

TIP120/TIP121/TIP122

Package: TO-220

ISSUE DATE:2000/4/27 REV:A

Parameter	Test Condition	Test Specification	
		Min	Max
V _{CEO(SUS)}	I _C =100mA _{dc} , I _B =0, TIP120	60V	-
	I _C =100mA _{dc} , I _B =0, TIP121	80V	-
	I _C =100mA _{dc} , I _B =0, TIP122	100V	-
I _{CEO}	V _{CE} =30V _{dc} , I _B =0, TIP120	-	0.5mA
	V _{CE} =40V _{dc} , I _B =0, TIP121	-	0.5mA
	V _{CE} =50V _{dc} , I _B =0, TIP122	-	0.5mA
I _{CB0}	V _{CB} =60V, I _E =0, TIP120	-	0.2mA
	V _{CB} =80V, I _E =0, TIP121	-	0.2mA
	V _{CB} =100V, I _E =0, TIP122	-	0.2mA
I _{EBO}	V _{BE} =5V, I _C =0, TIP120/TIP121/TIP122	-	2mA
h _{FE}	I _C =0.5A, V _{CE} =3V	1000	-
	I _C =3A, V _{CE} =3V	1000	-
V _{CE(sat)}	I _C =3A, I _B =12mA	-	2V
	I _C =5A, I _B =20mA	-	4V
V _{BE(on)}	I _C =3A, V _{CE} =3V	-	2.5V

MARKING:

