



### TEST SPECIFICATION

Plastic Medium-Power Complementary Silicon Transistor

TIP115/TIP116/TIP117

Package: TO-220

ISSUE DATE:2000/4/26 REV:A

Parameter	Test Condition	Test Specification	
		Min	Max
V <sub>CEO(SUS)</sub>	I <sub>C</sub> =30mA <sub>dc</sub> , I <sub>B</sub> =0, TIP115	60V	-
	I <sub>C</sub> =30mA <sub>dc</sub> , I <sub>B</sub> =0, TIP116	80V	-
	I <sub>C</sub> =30mA <sub>dc</sub> , I <sub>B</sub> =0, TIP117	100V	-
I <sub>CEO</sub>	V <sub>CE</sub> =30V <sub>dc</sub> , I <sub>B</sub> =0, TIP115	-	2mA
	V <sub>CE</sub> =40V <sub>dc</sub> , I <sub>B</sub> =0, TIP116	-	2mA
	V <sub>CE</sub> =50V <sub>dc</sub> , I <sub>B</sub> =0, TIP117	-	2mA
I <sub>CB0</sub>	V <sub>CB</sub> =60V, I <sub>E</sub> =0, TIP115	-	1mA
	V <sub>CB</sub> =80V, I <sub>E</sub> =0, TIP116	-	1mA
	V <sub>CB</sub> =100V, I <sub>E</sub> =0, TIP117	-	1mA
I <sub>EBO</sub>	V <sub>BE</sub> =5V, I <sub>C</sub> =0, TIP115/TIP116/TIP117	-	2mA
h <sub>FE</sub>	I <sub>C</sub> =1A, V <sub>CE</sub> =4V	1000	-
	I <sub>C</sub> =2A, V <sub>CE</sub> =4V	500	-
V <sub>CE(sat)</sub>	I <sub>C</sub> =2A, I <sub>B</sub> =8mA	-	2.5V
V <sub>BE(on)</sub>	I <sub>C</sub> =2A, V <sub>CE</sub> =4V	-	2.8V

MARKING:

