

LM7596 Simple Switcher Power Converter 150kHz 3A Step-Down Voltage Regulator

DESCRIPTION

The LM7596 series of regulators are monolithic integrated circuits that provide all the active functions for a step-down switching regulator, capable of driving a 3A load with excellent line and load regulation. These devices are vailable in fixed output voltages of 3.3V, 5V, 12V and an adjustable output version.

Requiring a minimum number of external components, these regulators are simple to use.

The LM7596 series operates at a switching frequency of 150kHz. Available in standard 5-lead TO-220 package.

Other features include a guaranteed \pm 3% tolerance on output voltage under specified input voltage and output load conditions, and \pm 15% on the oscillator frequency. External shutdown is included, featuring typically 100µA standby current. Self protection

features include a two stage frequency reducing current limit for output switch and an over temperature shutdown for complete protection under fault conditions. The over temperature shutdown level is about 145°C with 5°C hysteresis.

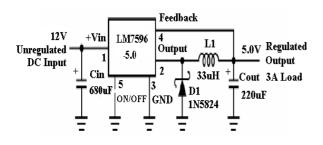
FEATURES

- 3.3V, 5V, 12V, and adjustable output versions
- Adjustable version output voltage range, 1.2V to 37V ±3% max over line and load conditions
- Available in TO-220
- · Guaranteed 3A output load current
- Input voltage range up to 40V
- Requires only 4 external components
- Excellent line and load regulation specifications
- 150 kHz fixed frequency internal oscillator
- TTL shutdown capability
- Low power standby mode, I_Ω typically 100 µA
- High efficiency
- Thermal shutdown and current limit protection

APPLICATIONS

- Simple high-efficiency step-down (buck) regulator
- On-card switching regulators
- Positive to negative converter

TYPICAL APPLICATION (Fixed Output Voltage Versions)



Adjustable Output Voltage Versions CFF LOCATE THE PROGRAMMING RESISTORS NEAR THE FEEDBACK PIN USING SHORT LEADS KEEP FEEDBACK WIRING AWAY FROM INDUCTOR FLUX UNREQULATED DC INPUT HEAVY LINES MUST BE KEPT SHORT AND USE GROUND PLANE CONSTRUCTION FOR BEST RESULTS

CONNECTION DIAGRAMS AND ORDERING INFORMATION

Bent and Staggered Leads, Through Hole Package 5-Lead TO-220 Side View GND Fins 1, 3, and 5 Pins 2 and 4 Fins 2 and 4 Fins 2 and 4 Fins 3 - Ground County 1 - V_{IN}



Absolute Maximum Ratings

Maximum Junction Temperature

Maximum Supply Voltage45VON /OFF Pin Input Voltage $-0.3 \le V \le +25V$ Feedback Pin Voltage $-0.3 \le V \le +25V$ Output Voltage to Ground-1VStorage Temperature Range -65° C to $+150^{\circ}$ CPower DissipationInternally limited

Operating Conditions

Temperature Range $-40^{\circ}\text{C} \le \text{T}_{\text{J}} \le +125^{\circ}\text{C}$ Supply Voltage 4.5V to 40V

ELECTRICAL CHARACTERISTICS

Unless otherwise specified, Tj=25°C. V_{IN} = 12V for the 3.3V, 5V and Adjustable version and V_{IN} = 24V for the 12V version. $I_{I,OAD}$ = 500mA.

+150°C

Characteristic	Symbol	Test Conditio	n	Min	Тур	Max	Unit
		LM7596-3.0	$4.75V \le V_{IN} \le 40V$,	3.20	3.30	3.40	
			$0.2A \le I_{LOAD} \le 3A$				
		LM7596-5.0	$7V \le V_{IN} \le 40V$,	4.85	5.0	5.15	
Output Voltage	V_{OUT}		$0.2A \le I_{LOAD} \le 3A$				V
		LM7596-12	$15V \le V_{IN} \le 40V,$	11.64	12.0	12.36	
			$0.2A \le I_{LOAD} \le 3A$				
		LM7596-3.0	$I_{OAD} = 3A$		73		
Efficiency	η	LM7596-5.0	$I_{OAD} = 3A$		80		%
		LM7596-12	$V_{IN} = 24V, I_{LOAD} = 3A$		90		
		LM7596-Adj	$V_{IN} = 12V, V_{OUT} = 3V,$		73		
			$I_{LOAD} = 3A$				
			$4.5V \le V_{IN} \le 40V,$				
Feedback Voltage	$ m V_{FB}$	LM7596-Adj	$0.2A \le I_{LOAD} \le 3A$,	1.210	1.230	1.250	V
			V _{OUT} programmed for 3V				
Feedback Bias Current	Ib	LM7596-Adj	$V_{FB} = 1.3V$		15	50	nA
Oscillator Frequency	f_{O}			127	150	173	kHz
Saturation Voltage	V_{SAT}	$I_{OUT} = 3A \text{ (Note 1,2)}$			1.16	1.4	V
Max Duty Cycle (ON)	DC	(Note 2)			100		%
Min Duty Cycle (OFF)		(Note 3)			0		
Current Limit	I_{CL}	Peak Current (Note 1,2)		3.4	4.5	6.0	A
Output Leaked Current	I_{L}	Output = $0V$ (Note 1,3)				50	μΑ
		Outpu	$t = -1V, V_{IN} = 40V$		2	30	mA
Quiescent Current	I_Q	(Note 3)			5	10	mA
Standby	I_{STBY}	$ON/OFF pin = 5V (OFF), V_{IN} = 40V$			100	200	μΑ
Quiescent Current							
ON/OFF Pin Logic					1.3		
Input							
Threshold Voltage	V_{IH}	Low (Regulator ON)				0.6	V
	$V_{ m IL}$		n (Regulator OFF)	2.0			
ON/OFF Pin Input	I_{H}	$V_{LOGIC} = 2.5V$ (Regulator OFF)			5	15	μΑ
Current	$ m I_L$	$V_{LOGIC} = 0.5V$ (Regulator ON)			0.02	5	

Note 1: No elements connected to output pin.

Note 2: Feedback pin removed from output and connected to 0V to force the output transistor switch ON.

Note 3: Feedback pin removed from output and connected to 12V for the 3.3V, 5V, and the A version, and 15V for the 12V version. To force the output transistor switch OFF.

PHYSICAL CHARACTERISTICS

 \bullet Wafer Diameter.....100 \pm 0.5 mm

 \bullet Wafer Thickness.....350 \pm 20 μm

• Scribe Width...... 100 μm

• Passivation.....PSG

• Metallization bottom...... Ti-Ni-Ag

Ti-Ni – 0.5-0.7μm

 $Ag - 0.6-0.1 \mu m$

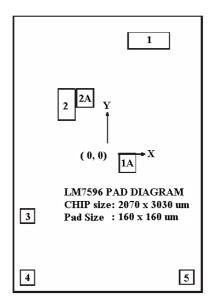
PAD LOCATION COORDINATES

Pad	Pad Name	X	Υ
No		(μ m)	(μ m)
1	V_{IN}	463	1252
1A	V _{IN}	225	-95
2	OUTPUT	-445	547
2A	OUTPUT	-245	615
3	GROUND	-875	-120
4	FEEDBACK	-875	-1355
5	ON/OFF	875	-1355

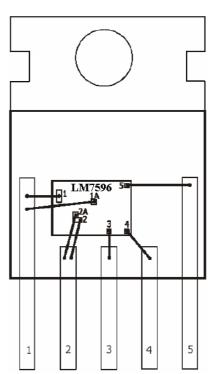
Note:

- ◆ The chip substrate is electrically connected to GND (Pad 3)
- ♦ Pads 1 and 1A are internally connected
- ◆ Pads 2 and 2A are internally connected

LM7596 PAD LAYOUT



BONDING DIAGRAM



Note:

Wire diameter > 60 micron

PAD 1 and PAD 1A (V_{IN}) are connected to the Pin1.

PAD 2 and PAD 2A (OUTPUT) are connected to the Pin 2.

PAD 3 (GROUND) is connected to the Pin 3.

PAD 4 (FEEDBACK) is connected to the Pin 4.

PAD 5 (ON/OFF) is connected to the Pin 5.